

Appl. No. 10/605,513
Amdt. dated March 17, 2005
Reply to Office action of November 17, 2004

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

- 1 (currently amended): A nitride based light emitting diode (LED) comprising:
- 5 a substrate;
- a light emitting stacked structure formed over the substrate;
- a nitride based dual dopant contact layer formed over the light emitting stacked structure,
- the nitride based dual dopant contact layer comprising a plurality of p-type dopants
- and a plurality of n-type dopants; and
- 10 a transparent conductive oxide layer formed over the nitride based dual dopant contact layer.
- 2 (currently amended): The LED of claim 1, wherein the nitride based dual dopant contact layer is made of AlInGa_N-based material, the transparent conductive oxide layer is
- 15 made of ~~Indium~~ indium-tin oxide (ITO), ~~Cadmium~~ cadmium-tin oxide, ~~Antimony~~ antimony-tin oxide (ATO), ~~Zinc~~ zinc oxide (ZnO), or ~~Zinc~~ zinc-tin oxide.
- 3 (currently amended): The LED of claim 1, wherein the nitride based dual dopant contact layer is formed by adding the p-type dopants and the n-type dopants
- 20 together through an epitaxy growth.
- 4 (currently amended): The LED of claim 1, wherein the nitride based dual dopant contact layer is formed by: providing a second conductive type contact layer on the light emitting stacked structure; then providing a first conductive type contact layer on
- 25 the second conductive type contact layer; and then cooling the LED through a cooling rate less than 40°C/min.

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- 5 (currently amended): The LED of claim 1 wherein the substrate is an insulating substrate, the light emitting stacked structure A light emitting diode (LED) comprising:
- 5 ~~an insulating substrate;~~
 a buffer layer formed over the insulating substrate;
 a first conductivity type contact layer formed over the buffer layer, the first conductivity type contact layer ~~comprising a first upper surface and a second upper surface being~~ made of $\text{Al}_{x1}\text{In}_{y1}\text{Ga}_{1-(x1+y1)}\text{N}$ ($0 \leq x1 \leq 1$; $0 \leq y1 \leq 1$; and $0 \leq x1+y1 \leq 1$);
 - 10 ~~a multiple quantum well light emitting layer formed over the first upper surface conductivity type nitride based contact layer, and~~
 a second conductivity type contact layer formed over the multiple quantum well light emitting layer, the second conductivity type contact layer being made of $\text{Al}_{x2}\text{In}_{y2}\text{Ga}_{1-(x2+y2)}\text{N}$ ($0 \leq x2 \leq 1$; $0 \leq y2 \leq 1$; and $0 \leq x2+y2 \leq 1$);
 - 15 ~~a dual dopant contact layer formed over the second conductivity type contact layer, the dual dopant contact layer comprising a plurality of p-type dopants and a plurality of n-type dopants;~~
~~a transparent conductive oxide layer formed over the dual dopant contact layer;~~
~~a second conductivity type electrode formed over the transparent conductive oxide layer;~~
 20 ~~and~~
~~a first conductivity type electrode formed over the second upper surface.~~

- 6 (currently amended): The LED of claim 5, wherein the insulating substrate is made of one material selected from a material group consisting of sapphire, LiGaO_2 , [[or]]
 25 and LiAlO_2 ; the buffer layer is made of AlInGaN-based material or II-nitride-based material; the second conductivity type contact layer is made of GaN, AlGaN, or InGaN; the first conductivity type contact layer is made of GaN, AlGaN, or InGaN; the transparent conductive oxide layer is made of Indium tin oxide (ITO);

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~~Cadmium tin oxide, Antimony tin oxide (ATO), Zinc oxide (ZnO), or Zinc tin oxide;~~
~~the dual dopant contact layer is made of AlInGaN based material; the n-type~~
~~dopants are made of Si, Ge, Sn, Te, O, S, or C; and the p-type dopants are made of~~
~~Mg, Zn, Be, or Ga.~~

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7 (original): The LED of claim 5, wherein the multiple quantum well has r InGaN quantum wells and $(r+1)$ InGaN barriers, each InGaN quantum well is sandwiched in between two InGaN barriers, each InGaN quantum well is fabricated by $\text{In}_c\text{Ga}_{1-c}\text{N}$, and each InGaN barrier is made of $\text{In}_f\text{Ga}_{1-f}\text{N}$, $r \geq 1$, and $0 \leq f < c \leq 1$.

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8 (original): The LED of claim 5 further comprising a first conductivity type cladding layer interposed between the first conductivity type contact layer and the multiple quantum well light emitting layer and the first conductivity type cladding layer is made of $\text{Al}_x\text{Ga}_{1-x}\text{N}$, and $0 \leq x \leq 1$.

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9 (original): The LED of claim 5 further comprising a second conductivity type cladding layer interposed between the second conductivity type contact layer and the multiple quantum well light emitting layer and the second conductivity type cladding layer is made of $\text{Al}_z\text{Ga}_{1-z}\text{N}$, and $0 \leq z \leq 1$.

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10 (currently amended): The LED of ~~claim 5~~claim 1, wherein the substrate is a conductive substrate. ~~dual dopant contact layer is formed by adding the p-type dopants and the n-type dopants together through an epitaxial growth.~~

25 11 (currently amended): The LED of ~~claim 5~~claim 10 wherein the conductive substrate is made of one material selected from a material group consisting of GaN, SiC, Si, AlN, ZnO, MgO, GaP, GaAs, and Ge. ~~dual dopant contact layer is formed through a cooling rate less than $40^\circ\text{C}/\text{min}$.~~

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12-18 (cancelled).

19 (new): The LED of claim 1 wherein the nitride based dual dopant contact layer is
5 made of AlInGaN-based material; the n-type dopants are made of Si, Ge, Sn, Te, O,
S, or C; and the p-type dopants are made of Mg, Zn, Be, or Ca.